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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 1

### Complete if Known

Application Number	10/530,762
Filing Date	April 8, 2005
First Named Inventor	Maurits ORTMANNS <i>et al.</i>
Group Art Unit	To Be Assigned
Examiner Name	To Be Assigned
Attorney Docket Number	31583-216855

### U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
BKJ	1	4,509,037 A		HARRIS	04-02-1985	
BKJ	2	4,998,109 A		LECHEVALIER	03-05-1991	
BKJ	3	5,227,795 A		NISHIKAWA <i>et al.</i>	07-13-1993	
BKJ	4	4,737,702		KOENCK	04-12-1988	

### FOREIGN PATENT DOCUMENTS

Examiner Initials *	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sub>9</sub>
		Office	Number <sup>4</sup>	Code <sup>5</sup> (if known)				

### OTHER PRIOR ART - PENDING PATENT APPLICATIONS

Examiner Initials *	Cite No. <sup>1</sup>	
BKJ	5	VAN DER ZWAN, <i>et al.</i> , "A 0.2-MW CMOS Sigma Delta Modulator for Speech Coding with 80 DB Dynamic Range," <i>IEEE Journal of Solid-State Circuits</i> , IEEE Inc., New York, U.S., Vol. 31, No. 12, December 1, 1996 (pp. 1873-1880).
BKJ	6	LERCH <i>et al.</i> , "A Monolithic Sigma Delta A/D and D/A Converter with Filter for Broad-Band Speech Coding," <i>IEEE Journal of Solid-State Circuits</i> , IEEE Inc., New York, U.S., Vol. 26, No. 12, December 1, 1991 (pp. 1920-1926).
BKJ	7	U.S. 2002/011945 A1, LOCHER, January 31, 2002.
BKJ	8	WO 00/36750A1, QUALCOMM INCORPORATED, June 22, 2000.
BKJ	9	ORTMAN <i>et al.</i> , "Jitter Insensitive Feedback DAC for Continuous-Time Sigma-Delta Modulators," 2001 International Conference and Electronics, Circuits and Systems, ICECS 01. (no month given)

Examiner Signature

B.K.J.

Date Considered

3/23/06

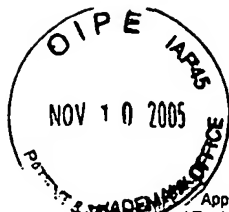
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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)				<b>Complete If Known</b>	
				Application Number	10/530,762-Conf. #6953
				Filing Date	April 8, 2005
				First Named Inventor	Maurits Ortmanns
				Art Unit	Not Yet Assigned
				Examiner Name	Not Yet Assigned
Sheet	1	of	1	Attorney Docket Number	31583-216855

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			

FOREIGN PATENT DOCUMENTS						
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		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)				

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
BK	CA	Maurits ORTMANNS et al., "CLOCK JITTER INSENSITIVE CONTINUOUS-TIME ΣΔ MODULATORS", IEEE, 2001, pages 1049-1052. No mention	
BK	CB	Maurits ORTMANNS et al., "NEUE DAU-RÜCKKOPPLUNG FÜR ZEITKONTINUIERLICHE ΣΔ MODULATOREN", ITG-Tagung, VDI, 2000, Pages 221-226. No mention	

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RK/cdw-696350

Examiner Signature	B.K. Yoon	Date Considered	3/23/06
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